**Physical Design**:

Input/Output interface

UART

SPI

CAN

Audio/Video Interfaces

3.5 mm audio

Connectivity

WiFi

Mobile data

Processor

CPU

Storage Interfaces

SD

Graphics

GPU

Memory Interface

NAND

DDR4

**Logical Design:**

Application

Management

Services

Security

Communication

Device

Reference**:**

1. [IOT\_logical and physical design pdf (google classroom)](https://drive.google.com/file/d/17WzluarvKM_Yd5MSCrzieKT36c2AZz6l/view)
2. [chatgtp](https://chatgpt.com/)
3. [Nevon projects IOT Projects Ideas](https://nevonprojects.com/iot-projects/)